

FIGURE 1 (System Block Diagram – Hardware)

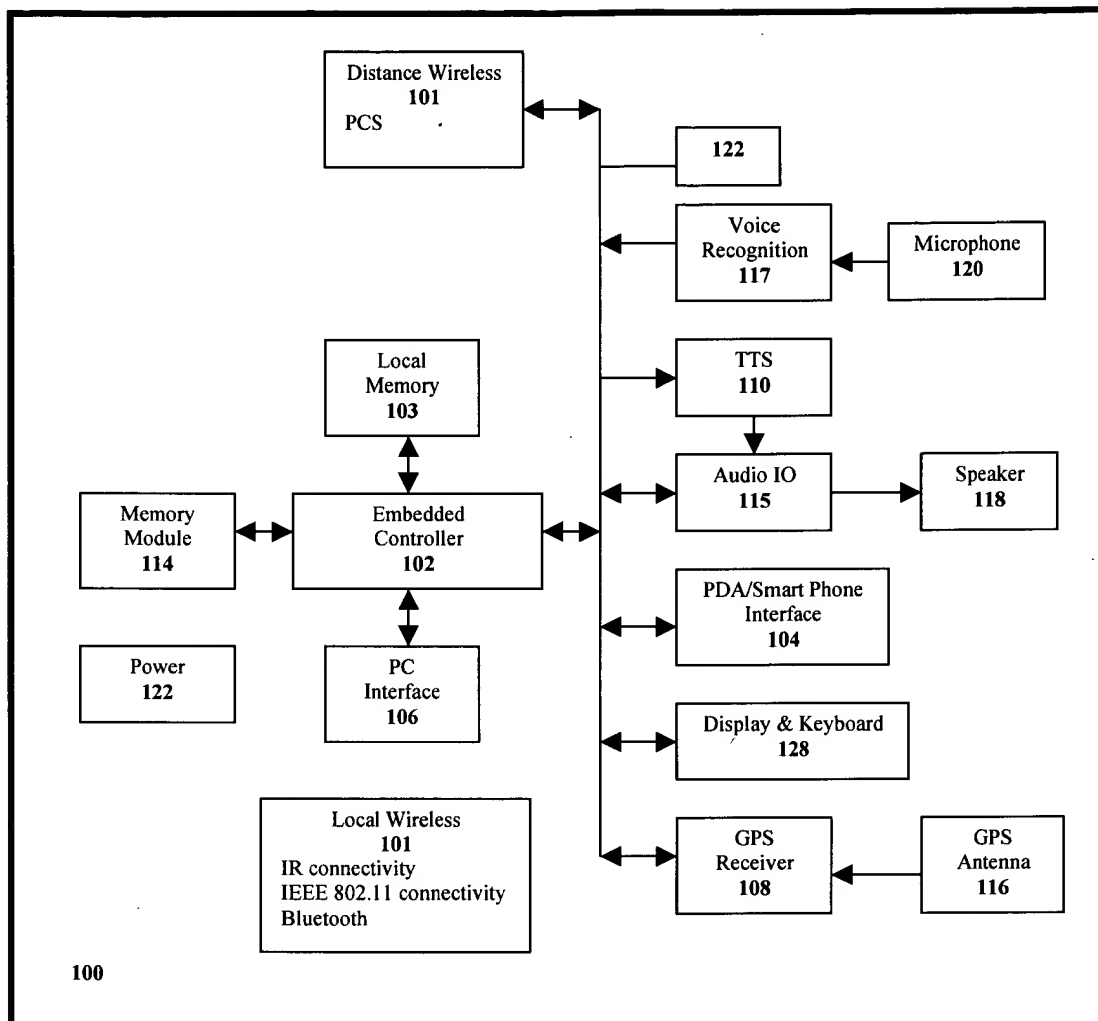




FIGURE 2 (System Block Diagram – Software)

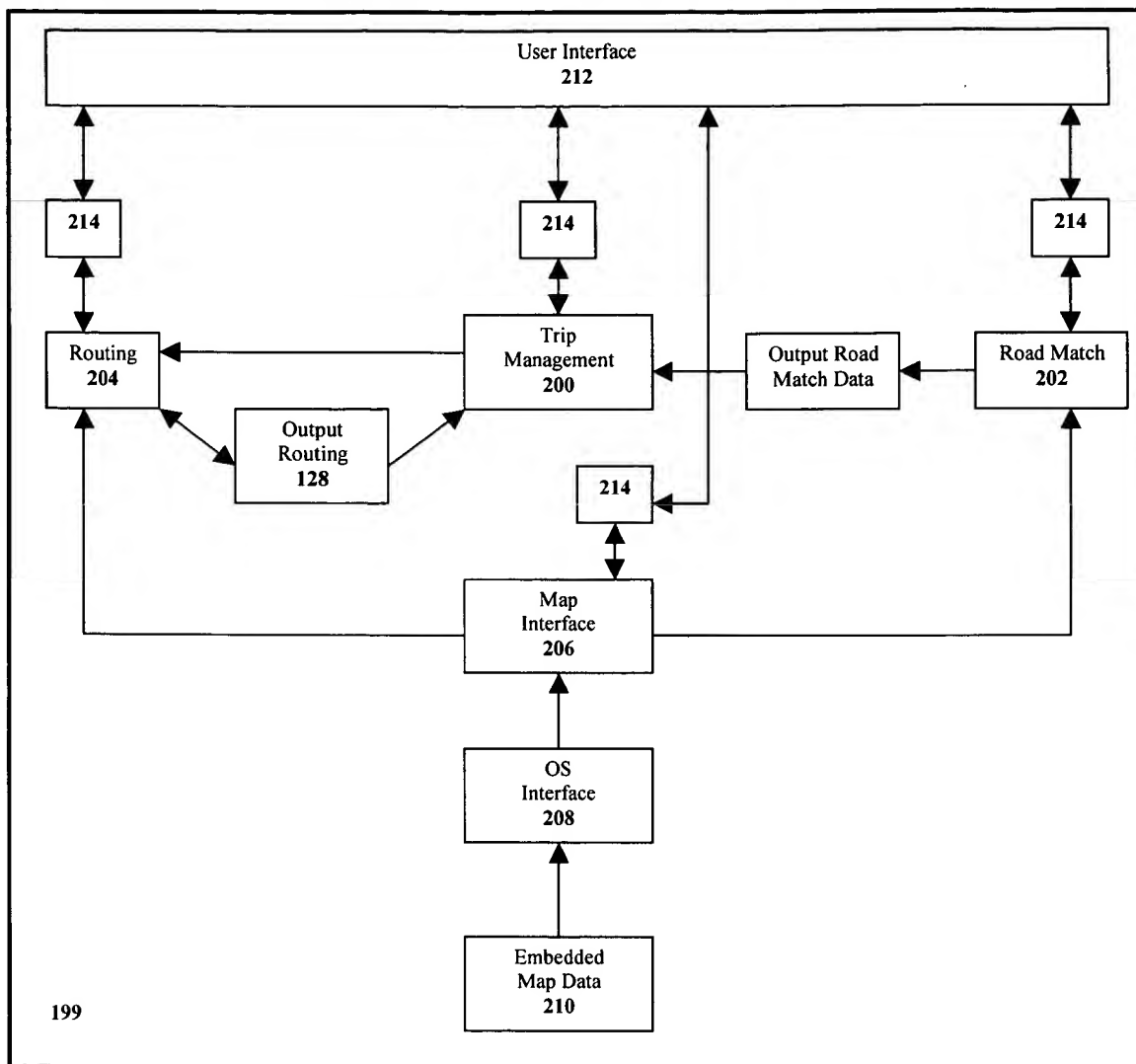




FIGURE 3 (Process of Creating Map Data)

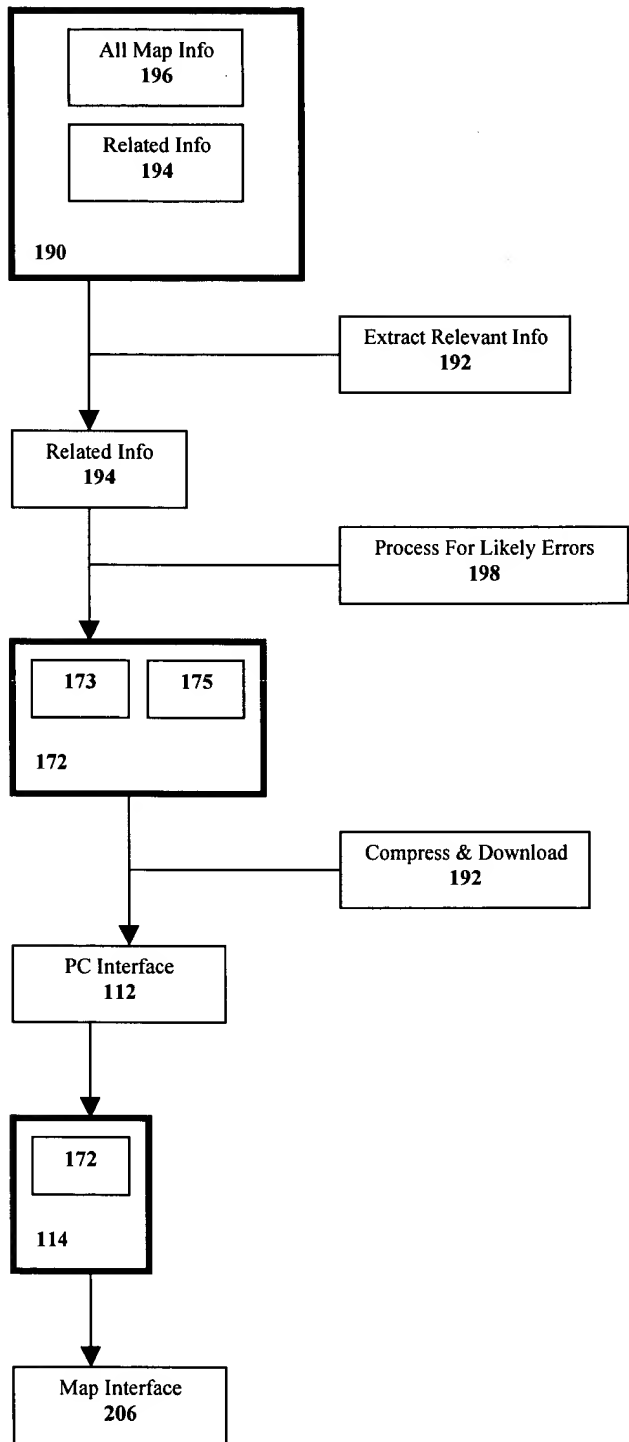




FIGURE 4 (Representation of 3-D Space Sensed by the Device)

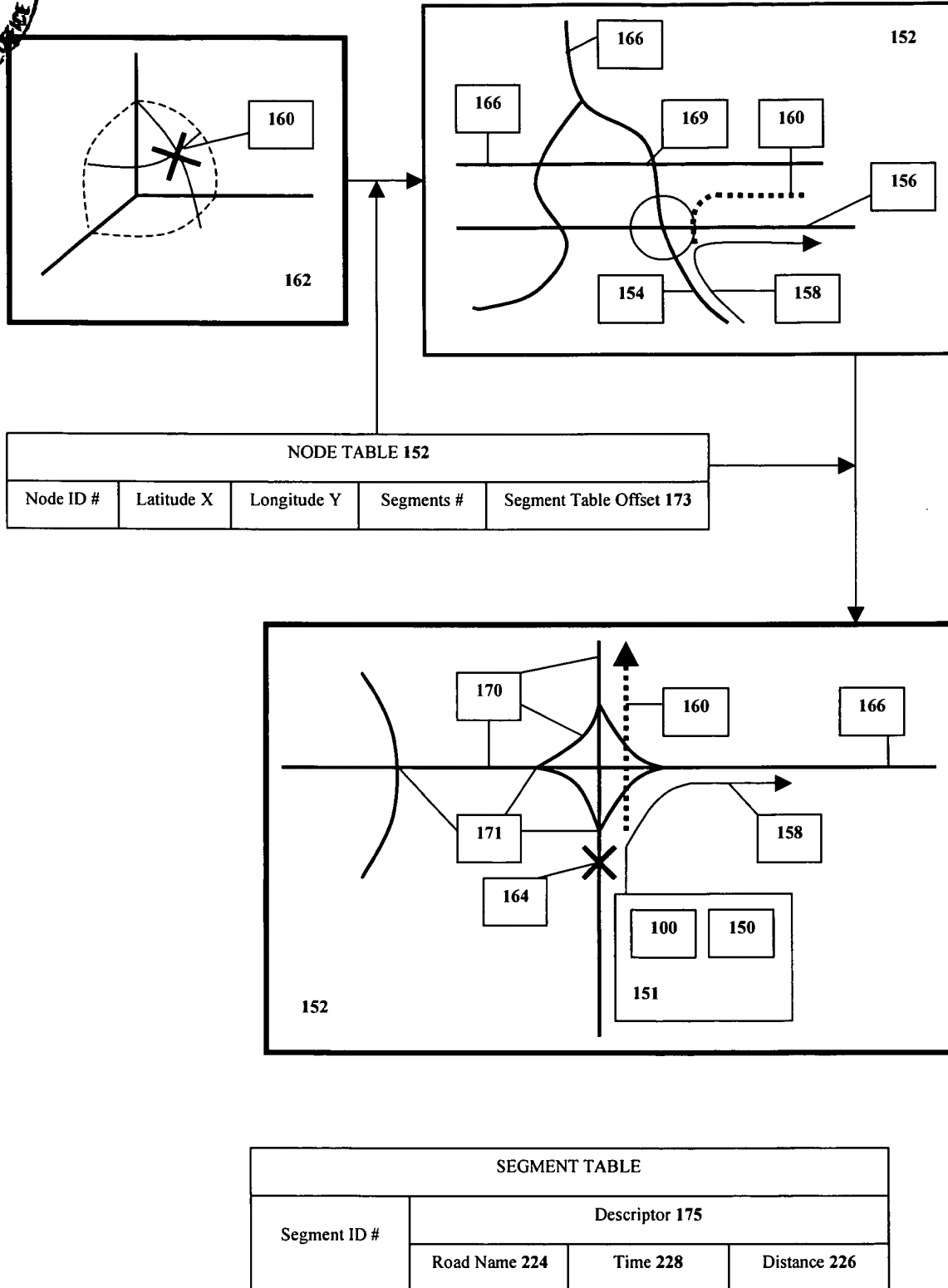


FIGURE 5 (Block Diagram of Multiple Device in Preferred Client/Server Mode)

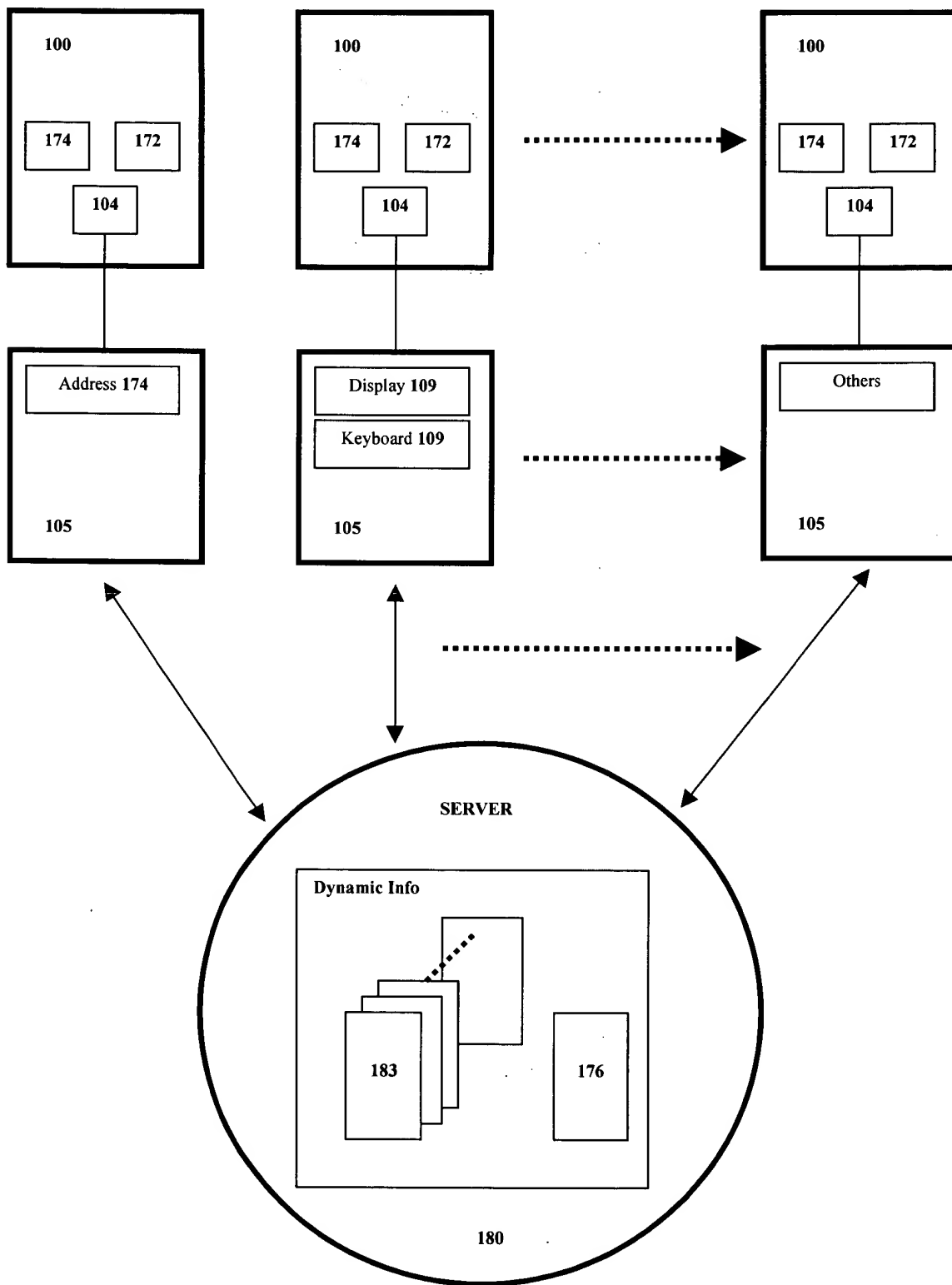




FIGURE 6 (Presentation of Box Search Structure for Acquisition and Tracking Modes for Road Match Software Module)

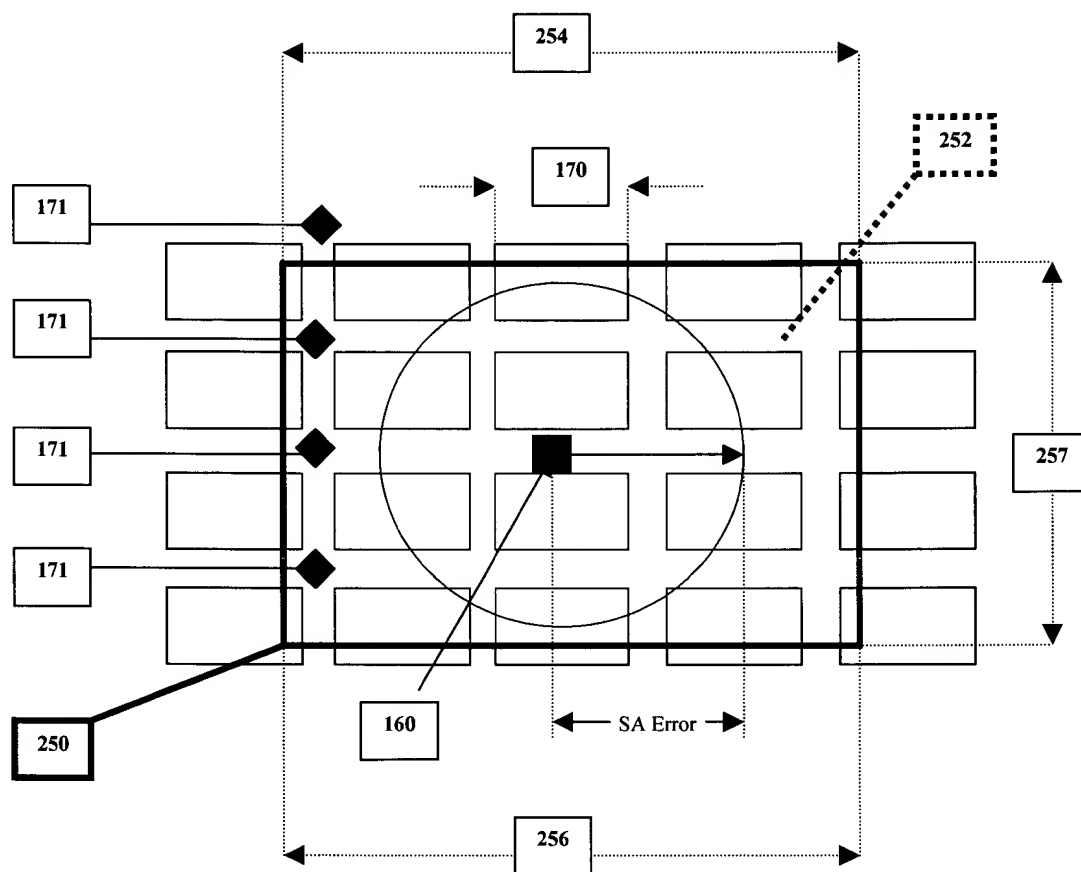




FIGURE 7 (Layers of Software & Associated Registers & Queues)

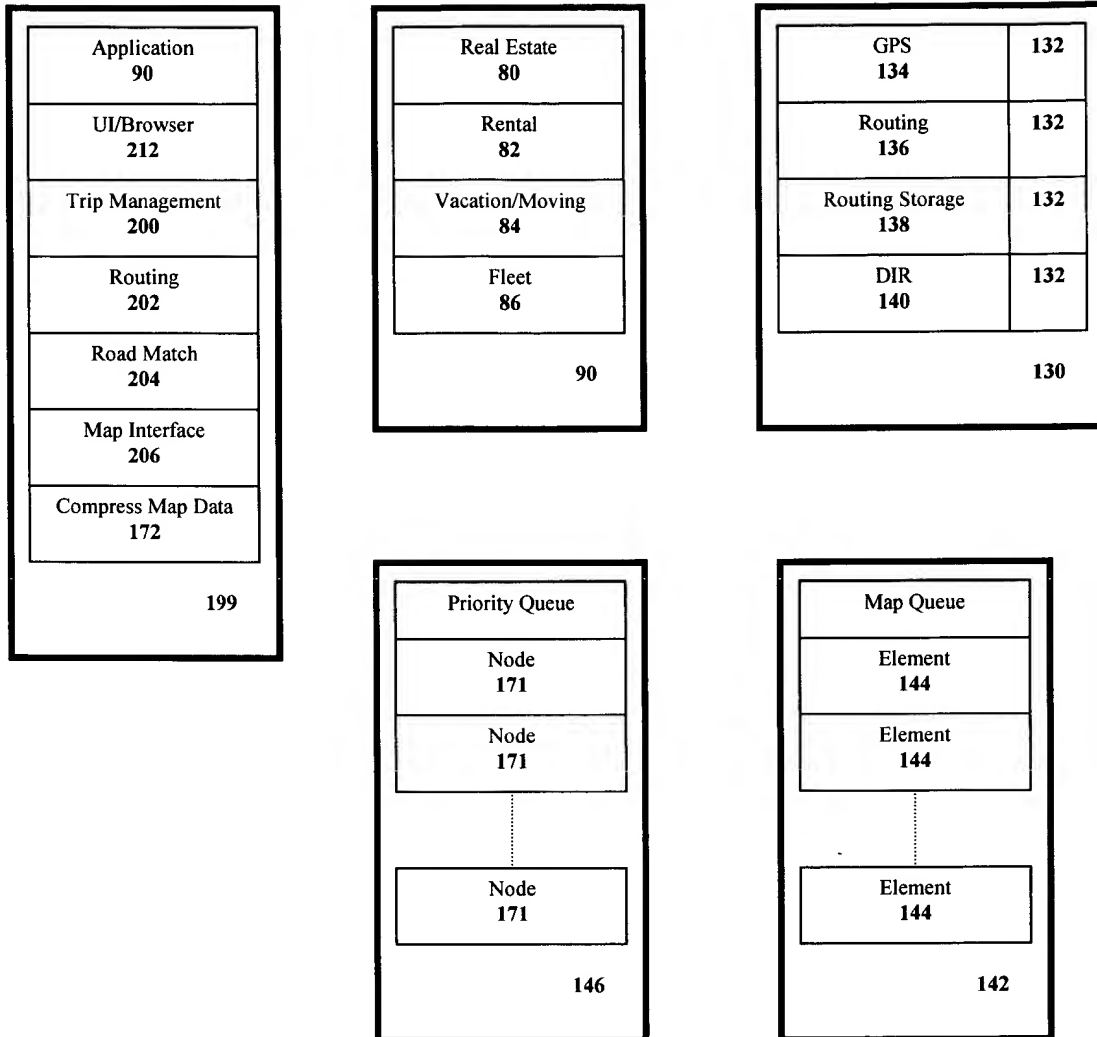




FIGURE 8 (Flow Chart of Map Match Process of Road Match Module)

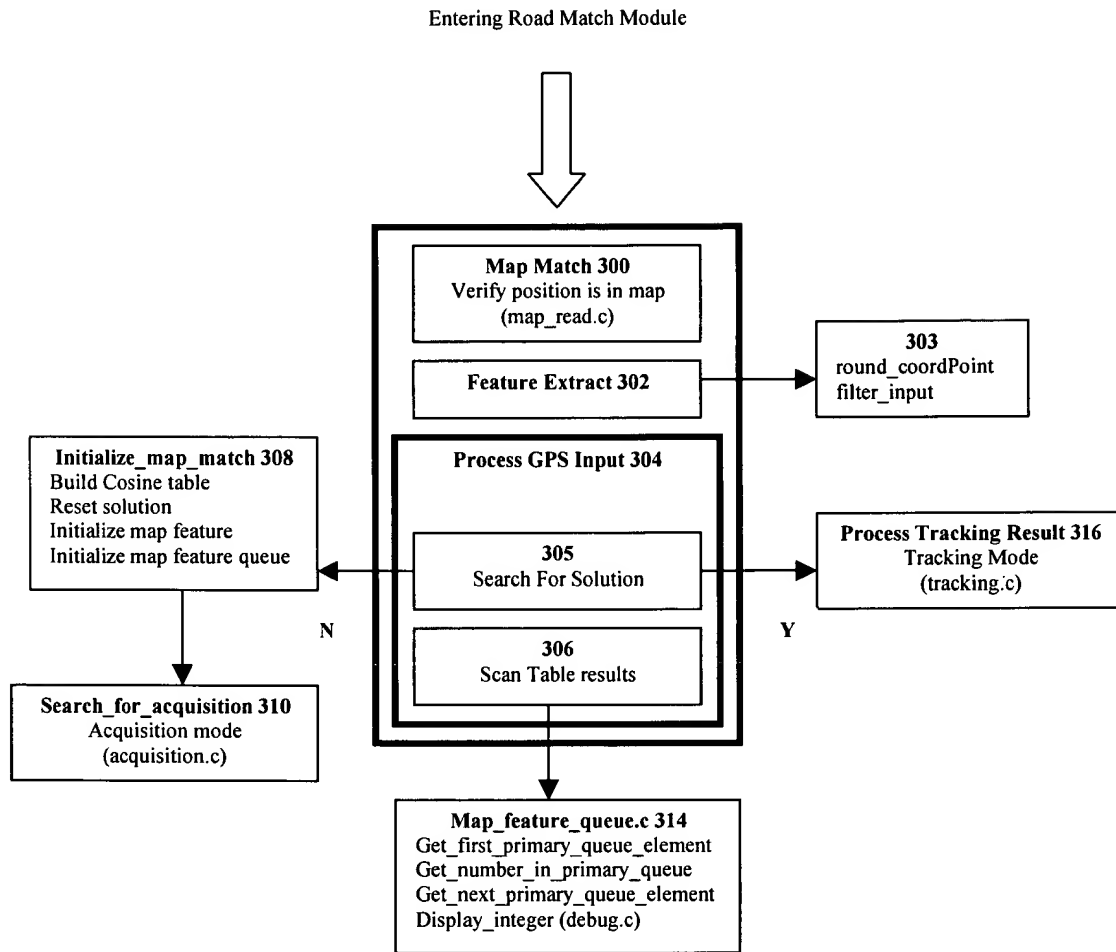






FIGURE 9 (Flow Chart of Tracking Mode Process of Road Match Module)

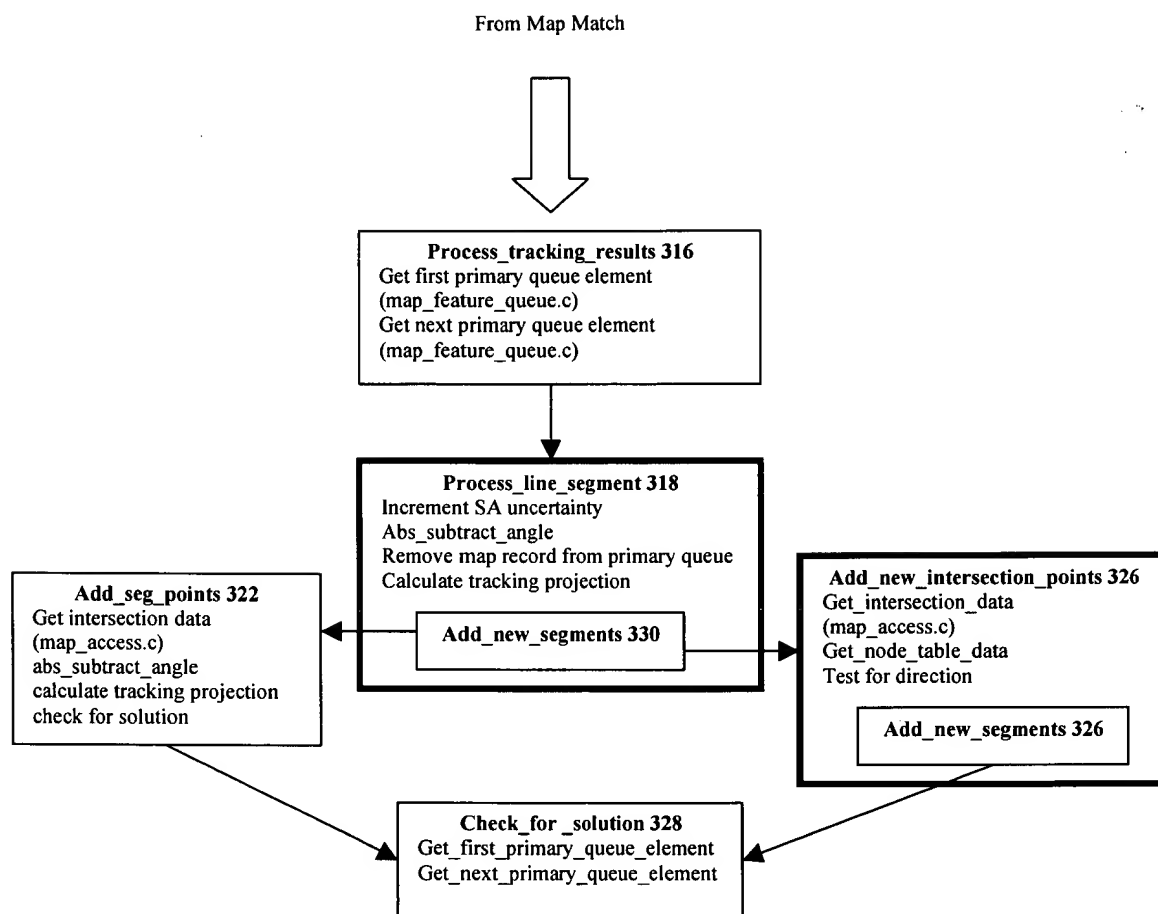




FIGURE 10 (Flow Chart of Acquisition Mode Process of Road Match Module)

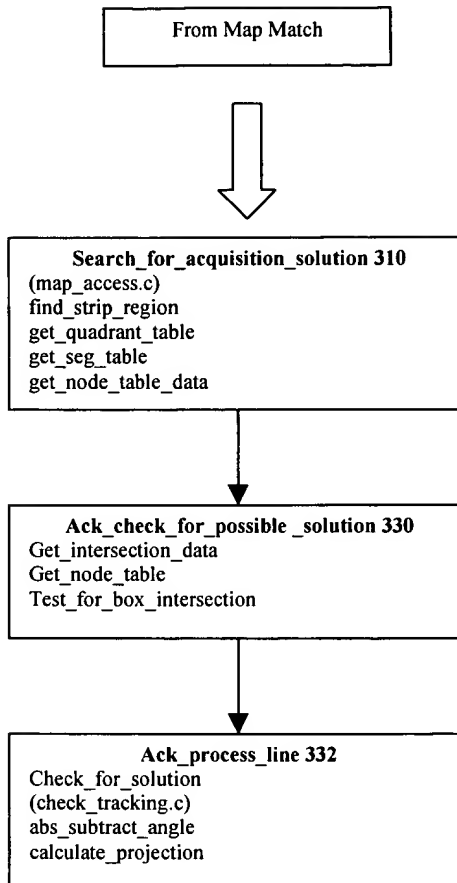


FIGURE 11 (Flow Chart of Processes of Trip Management Module)

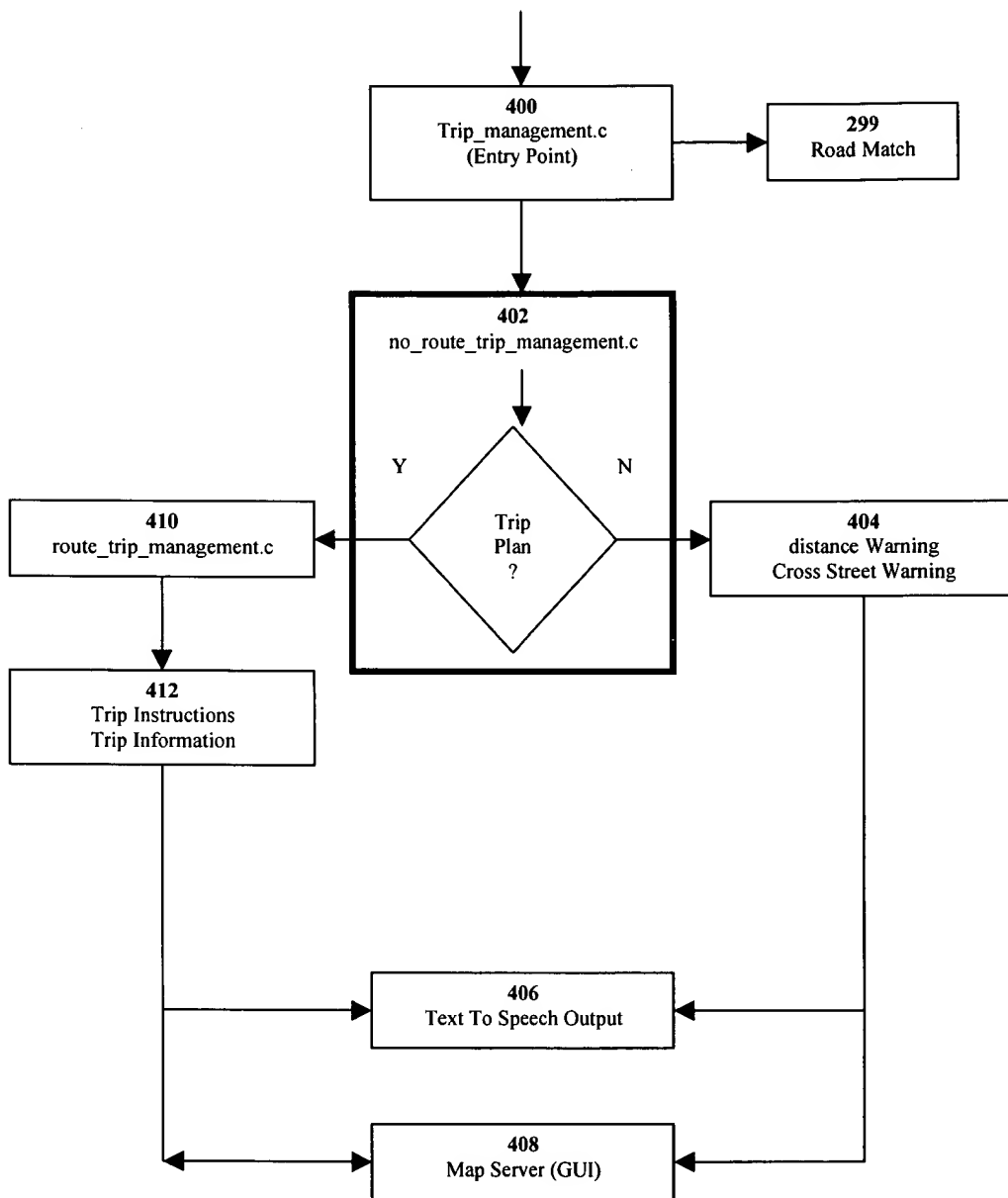




FIGURE 12 (Flow Chart of Data Flow of Routing Module)

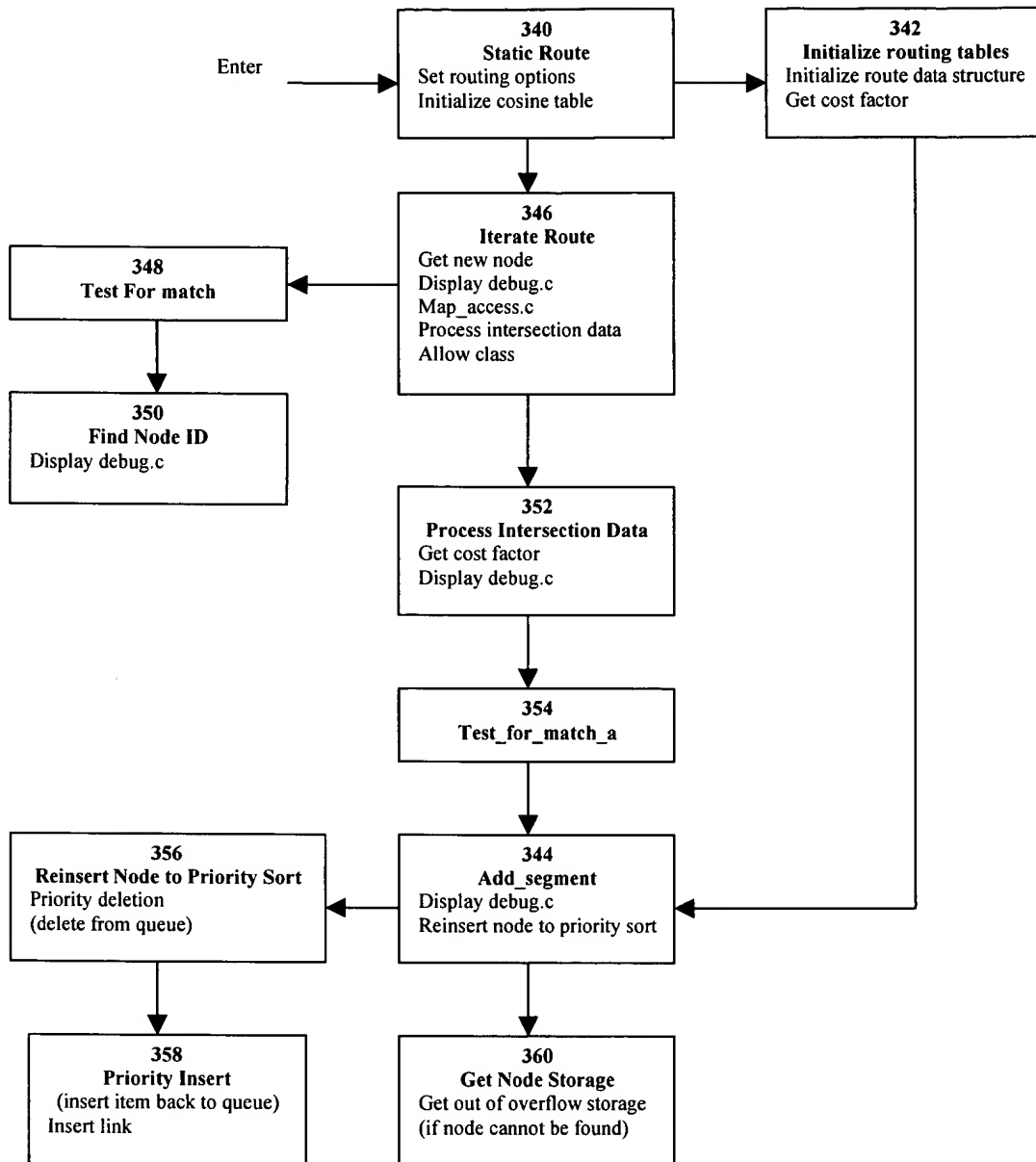




FIGURE 13 (Certain Data Structures Used By The Modules of Figure 2)

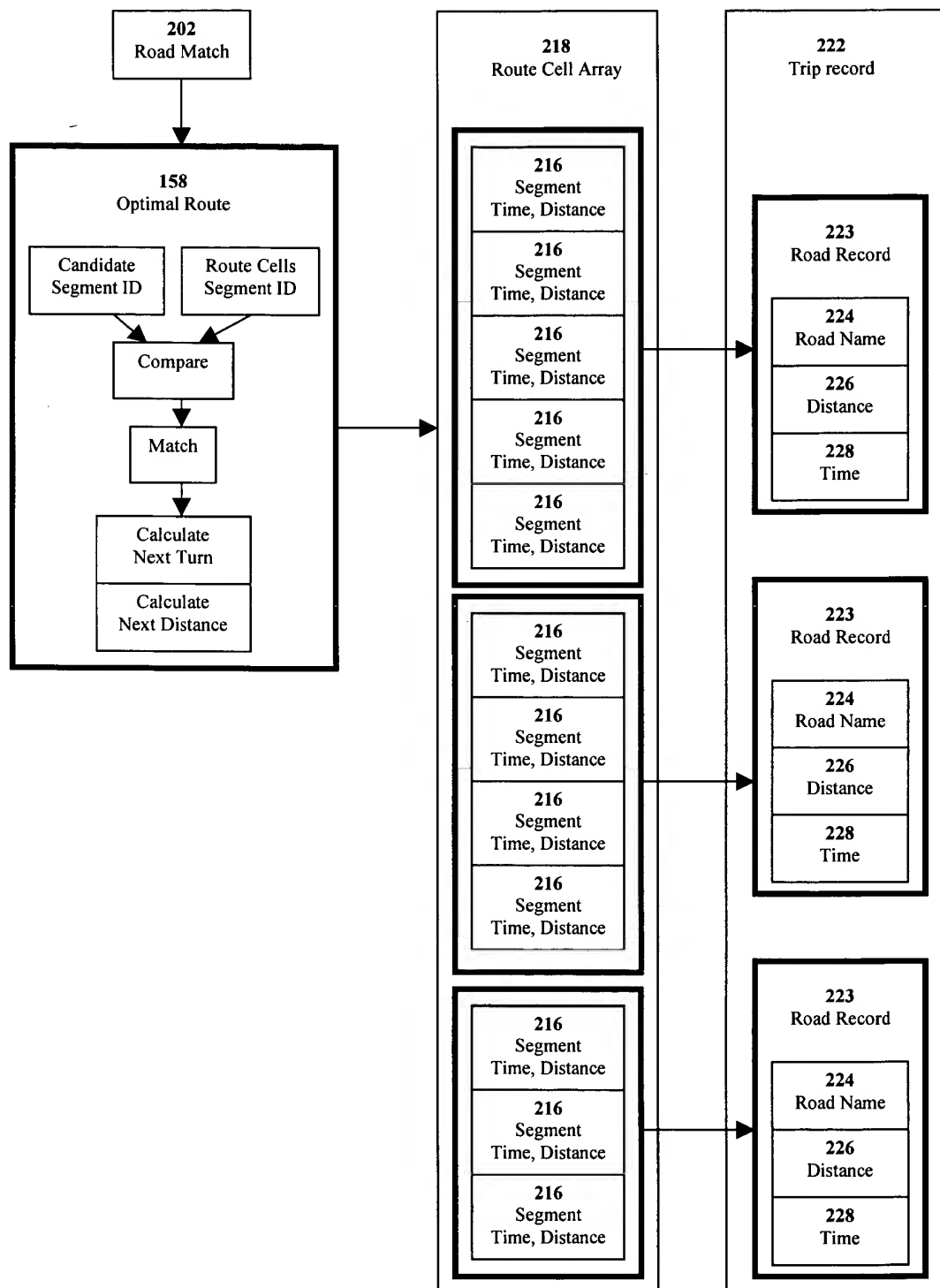




FIGURE 14 (Initial Routing Process of the Routing Module)

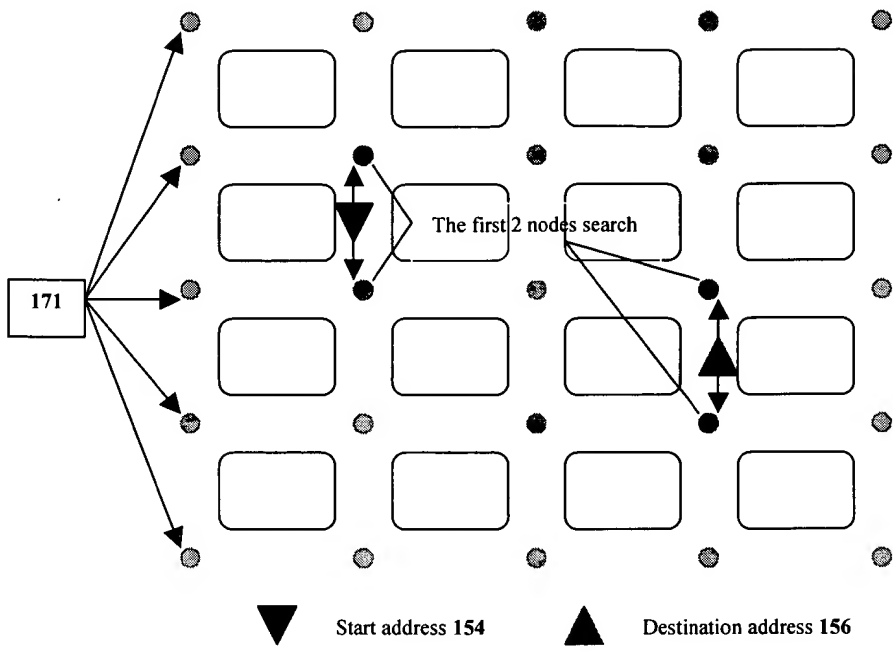


FIGURE 15 (Successive Routing Process to the Initial Routing Process)

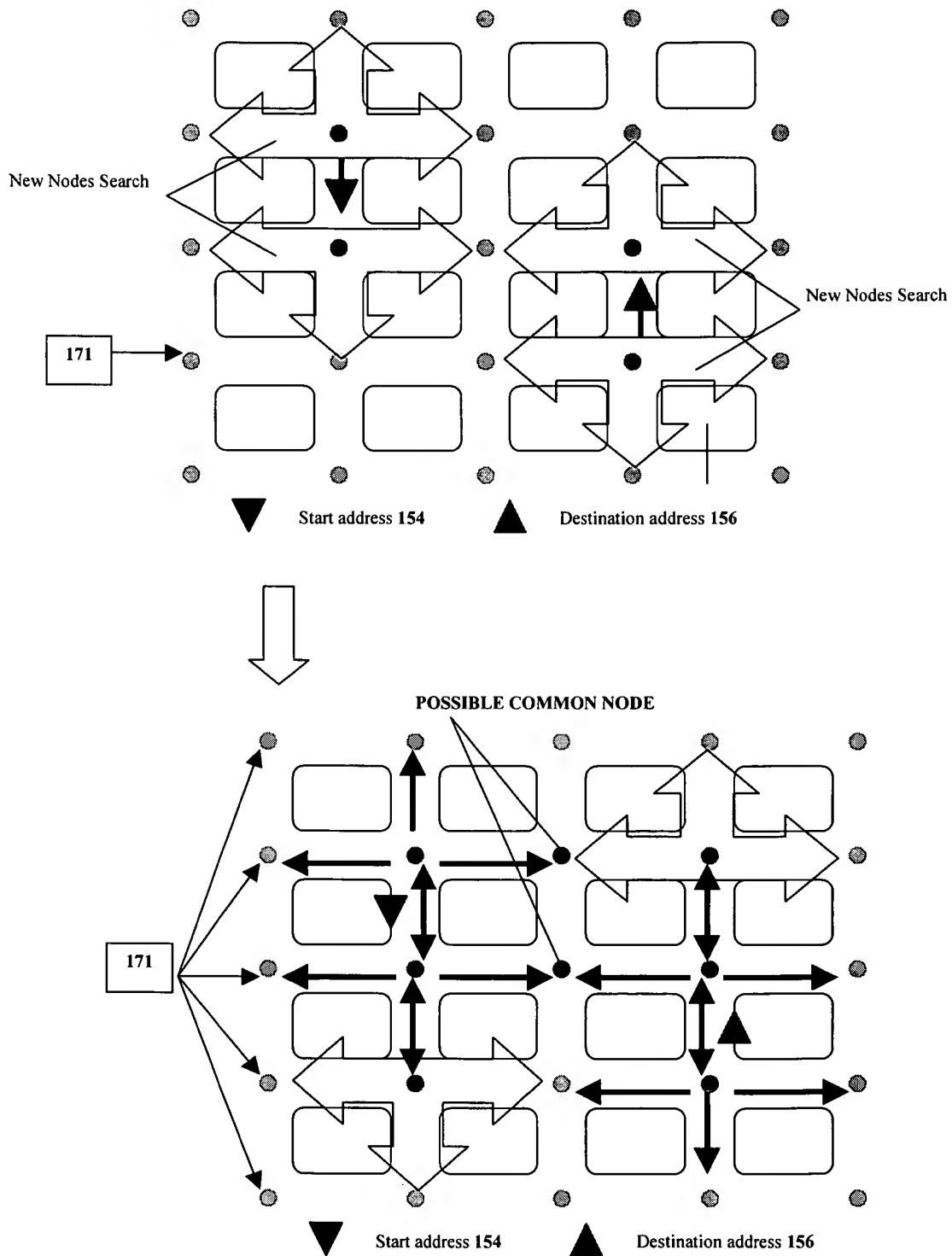




FIGURE 16 (Process of a Solution from Among the Routes Calculated by the Processes)

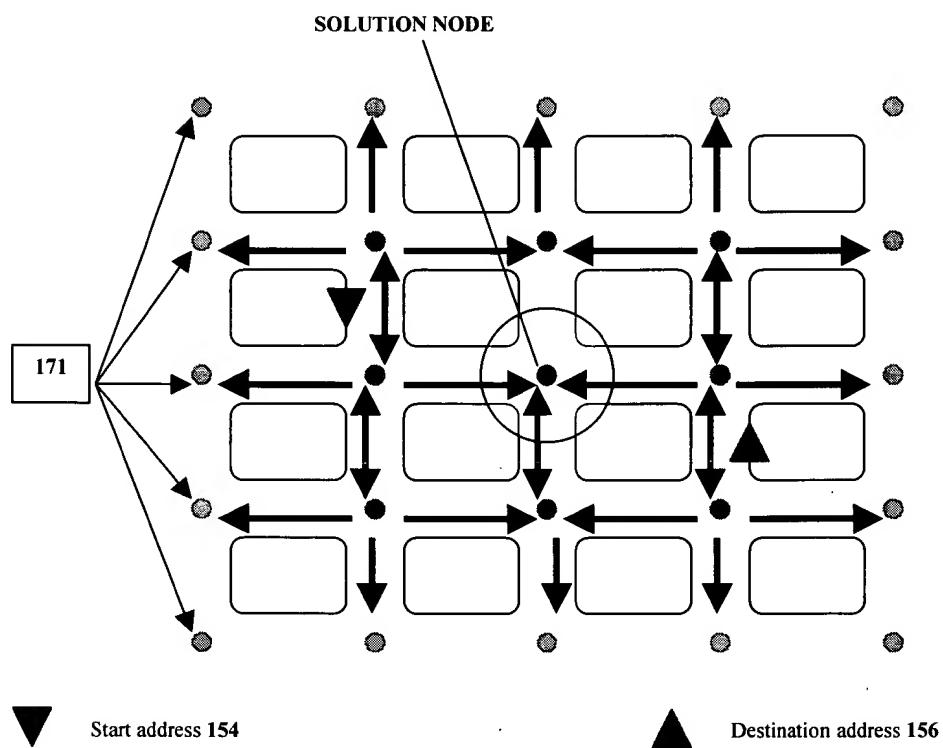






FIGURE 17 (Solution Chosen by The Routing Process)

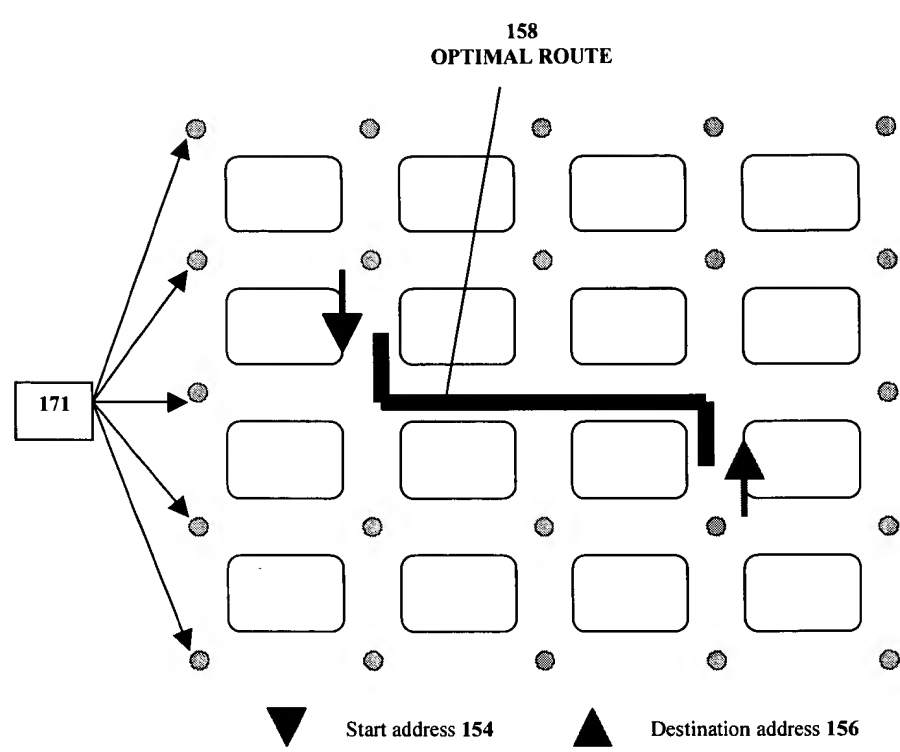




FIGURE 18 (Flow Chart of Some Processes of the Trip Management Module)

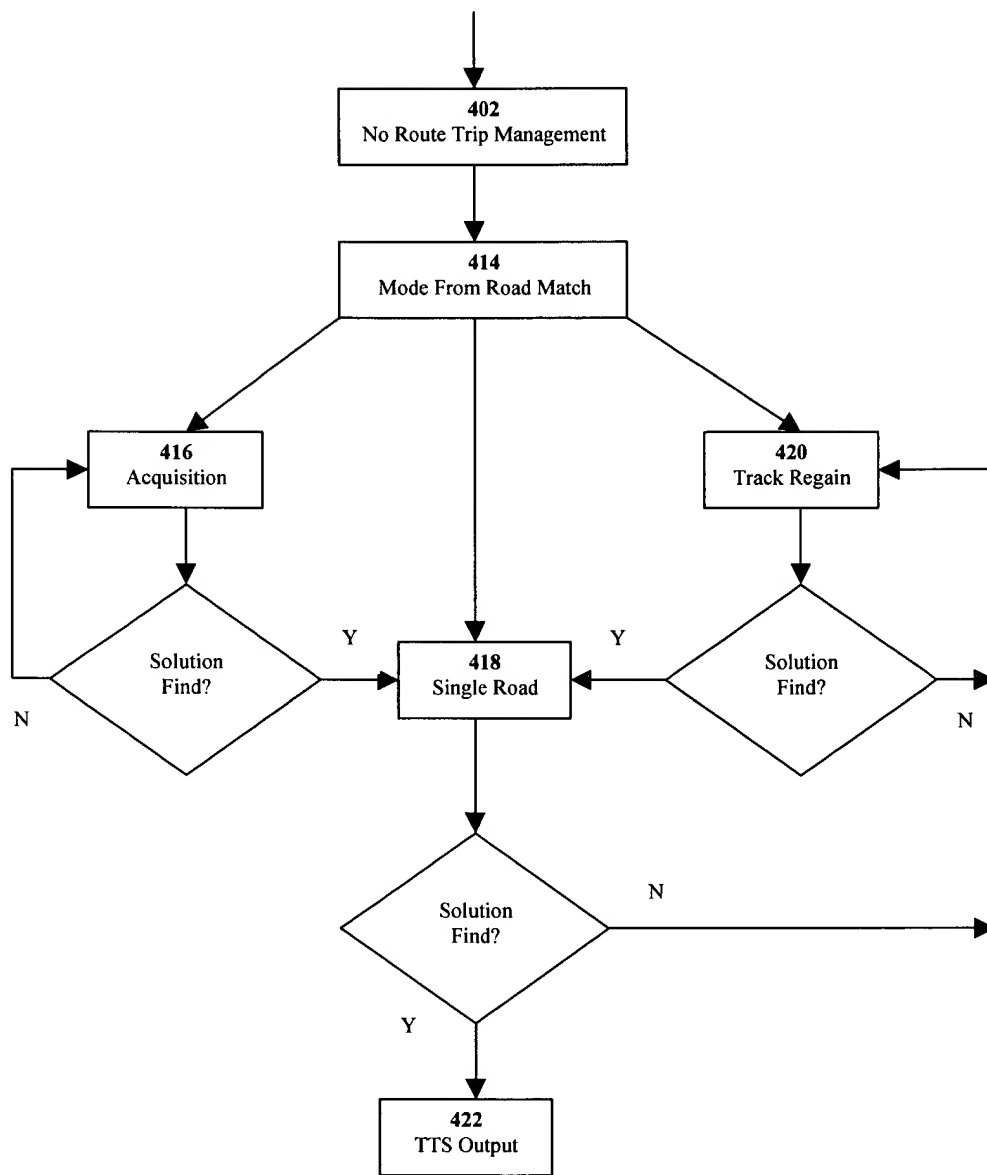




FIGURE 19 (Flow Chart of Some Processes of the Trip Management Module)

